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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

**Kim et al.**

Serial No.: **10/620,477**

Filed: **July 15, 2003**

For: **Adaptive Noise Filtering and  
Equalization for Optimal High  
Speed Multilevel Signal Decoding)**

Art Unit: **2816**

Examiner: **Lam, Tuan Thieu**

Confirmation No.: **8614**

**INFORMATION DISCLOSURE STATEMENT**

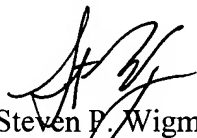
Commissioner for Patents  
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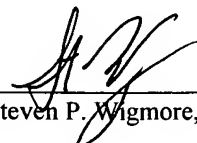
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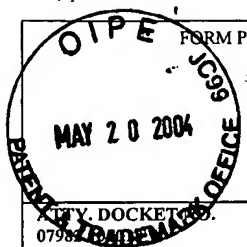
Respectfully submitted,

  
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K&S Docket: 07982.105018 US  
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FORM PTO-1449, Adapted

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CITY, DOCKET NO. 07982	SERIAL NO. 10/620,477	FILING DATE July 15, 2003
APPLICANT Kim et al.		GROUP 2816

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,181,136	1/19/1993	Kavehrad et al.			9/20/1990
	AB	5,625,722	4/29/1997	Froberg et al.			12/21/1994
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**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	NAME	TRANSLATION	
	AL	WO 02/067521 A1	8/29/2002	PCT	Vrazel et al.	YES	NO
	AM	WO 02/091600 A2	11/14/2002	PCT	Schmukler et al.		
	AN	WO 03/077423 A2	9/18/2003	PCT	Hietala et al.		
	AO	WO 03/092237 A1	11/6/2003	PCT	Vrazel et al.		
	AP	WO 2004/008782 A2	1/22/2004	PCT	Kim et al.		
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APPLICANT Kim et al.		GROUP 2816

**OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)**

BA	Choi et al.; <i>A 0.18-<math>\mu</math>m CMOS 3.5-Gb/s Continuous-Time Adaptive Cable Equalizer Using Enhanced Low-Frequency Gain Control Method</i> ; IEEE Journal of Solid-State Circuits; March 2004; Vol. 39, No. 3; pp. 419-425
BB	Paul, et al.; <i>3 Gbit/s Optically Preamplified Direct Detection DPSK Receiver With 116 photon/bit Sensitivity</i> ; Electronics Letters; Vol. 29, No. 7; April 1, 1993; pp. 614-615
BC	Penninckx et al.; <i>Optical Differential Phase Shift Keying (DPSK) Direct Detection Considered as a Duobinary Signal</i> ; Proc. 27 <sup>th</sup> Eur. Conf. on Opt. Comm. (ECOC'01 - Amsterdam); Vol. 3; September 30 to October 4, 2001; pp. 456-457
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